



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No. : **C 0332862** BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No. : 21074000544

Name of the College : C9-H M I T S, BOGARAM

Name : KOTTE RAJU

Month & Year of Final Exam : May, 2013

Hall Ticket No. : 09C91A0453

Year of Admission : 2009-2010

Class Awarded : FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	17	52	69	4	2	MATHEMATICS - I	20	33	53	6
3	MATHEMATICAL METHODS	22	33	55	6	4	ENGINEERING PHYSICS	15	26	41	4
5	ENGINEERING CHEMISTRY	19	26	45	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	21	40	61	6
7	ENGINEERING DRAWING	20	31	51	4	8	COMPUTER PROGRAMMING LAB	24	38	62	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	22	45	67	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	20	41	61	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	20	45	65	4						
I SEMESTER											
II YEAR											
1	MATHEMATICS-III	21	26	47	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	14	26	40 ^A	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	13	27	40	3	2	ELECTRONIC CIRCUIT ANALYSIS	13	31	44	4
3	ENVIRONMENTAL STUDIES	13	46	59	3	3	PULSE & DIGITAL CIRCUITS	13	27	40	4
4	ELECTRIC CIRCUITS	15	26	41	4	4	SWITCHING THEORY & LOGIC DESIGN	16	38	54	4
5	ELECTRONIC DEVICES & CIRCUITS	05	49	54	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	14	26	40	4
6	SIGNALS & SYSTEMS	14	26	40	4	6	ELECTRICAL ENGINEERING LAB	15	35	50	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	13	21	34	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	21	18	39	2
8	BASIC SIMULATION LAB	15	39	54	2	8	PULSE & DIGITAL CIRCUITS LAB	16	28	44	2
I SEMESTER											
III YEAR											
1	CONTROL SYSTEMS	14	31	45	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	17	44	61	4
2	COMPUTER ORGANIZATION	20	31	51	4	2	NANO TECHNOLOGY	20	26	46	4
3	ANTENNAS & WAVE PROPAGATION	17	29	46	3	3	DIGITAL COMMUNICATIONS	14	35	49	3
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	09	37	46	4	4	MICROPROCESSORS & MICROCONTROLLERS	18	47	65	4
5	ANALOG COMMUNICATIONS	14	31	45	3	5	DIGITAL SIGNAL PROCESSING	16	33	49	4
6	IC APPLICATIONS	18	26	44	3	6	MICROPROCESSORS & MICROCONTROLLERS (LAB)	18	37	55	2
7	ANALOG COMMUNICATIONS (LAB)	14	30	44	2	7	DIGITAL SIGNAL PROCESSING (LAB)	23	42	65	2
8	IC APPLICATIONS (LAB)	15	42	57	2	8	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	20	45	65	2
I SEMESTER											
IV YEAR											
1	MANAGEMENT SCIENCE	19	36	55	3	1	CELLULAR & MOBILE COMMUNICATIONS	20	57	77	3
2	VLSI DESIGN	14	36	50	4	2	RADAR SYSTEMS	17	36	53	3
3	MICROWAVE ENGINEERING	25	26	51	3	3	DIGITAL DESIGN THROUGH VERILOG HDL	18	30	48	3
4	COMPUTER NETWORKS	20	50	70	4	4	INDUSTRY ORIENTED MINI PROJECT	-	45	45	2
5	DIGITAL IMAGE PROCESSING	19	36	55	3	5	SEMINAR	47	-	47	2
6	EMBEDDED SYSTEMS	17	37	54	4	6	MAJOR PROJECT	45	144	189	10
7	E-CAD AND VLSI LAB	22	44	66	2	7	COMPREHENSIVE VIVA	-	92	92	2
8	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	21	40	61	2						

(# Project Internal= 50, External=150)

Number of Credits registered for : 200 Aggregate Marks Secured for best: 194

Aggregate Marks Secured : 3116 OUT OF 5200 (59.92%) $\geq 59.5\%$ is treated as First Class as per amended Regulations)

Date of Issue : August 7, 2013
(see overleaf for Rules concerned to award of class)



CONTROLLER OF EXAMINATIONS

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)